Radial Electron Collection in Dye-Sensitized Solar Cells

Alex B. F. Martinson,^{†,‡} Jeffrey W. Elam,[‡] Jun Liu,[†] Michael J. Pellin,^{†,‡} Tobin J. Marks,[†] and Joseph T. Hupp^{*,†}

Department of Chemistry and Argonne-Northwestern Solar Energy Research Center, Northwestern University, 2145 Sheridan Road, Evanston, Illinois 60208, and Division of Materials Science, Argonne National Laboratory, 9700 South Cass Avenue, Argonne, Illinois 60439

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ABSTRACT

We introduce a new photoelectrode architecture consisting of concentric conducting and semiconducting nanotubes for use in dye-sensitized solar cells (DSSCs). Atomic layer deposition is employed to grow indium tin oxide (ITO) within a porous template and subsequently coat the high area photoelectrode with amorphous TiO₂. Compared with control devices lacking a current collector within the pores, the new photoelectrode geometry exhibits dramatically higher current densities, an effect attributed to the radial collection of electrons.

In the most efficient DSSCs to date, the superior light harvesting efficiency of photons throughout the visible spectrum is realized by a ~14 μ m thick nanoparticle photoanode with roughness >2000.¹ The implications of this architecture define DSSCs as a class of photovoltaics (PVs) with (1) an exceedingly large, heterogeneous interface through which electrons may be (parasitically) intercepted and with (2) slow electron transport controlled by trap-limited hopping through a relatively long and tortuous path to the transparent electrode.^{2,3}

Given a charge collection time of milliseconds at the maximum power point,⁴ efficient charge extraction is only made possible through the extraordinarily slow interception of electrons by adjacent I_3^- . Thus, what promises to be a tour de force in photoelectrochemical energy conversion has so far only offered a limited variable space in which efficient devices are realized. Particularly lacking is an assortment of redox couples that permit reasonable collection efficiency, the current number able to be counted upon one hand.⁵⁻⁸ Furthermore, the most successful shuttle to date, iodide/ triiodide (I⁻/I₃⁻), builds in photovoltage losses of approximately 500 to 600 mV on account of the high driving force needed to regenerate dyes at acceptable rates. Additionally, the iodine-based redox shuttle remains poorly understood owing to its multielectron nature and nonconformity to simple rate laws.9 One consequence of the "shuttle problem" has been efficiency improvements of only $\sim 1\%$ in DSSC devices over the past 15 years.^{1,10}

In order to accommodate the faster electron interception rates of alternative redox shuttles, the collection time of electrons traversing the photoanode must be reduced. In the limit of rapid dye regeneration, the collection of electrons at the transparent conducting electrode (TCO) is in competition only with the interception of electrons by the oxidizing redox species. Since electron transport through the wide bandgap semiconductor is typically diffusion controlled, the transport time is inversely proportional to the effective electron diffusion constant and goes as the inverse square of photoanode thickness. Therefore, two distinct strategies may make devices more tolerant to faster interception (i.e., alternative redox couples), accelerating electron diffusion or reducing the photoanode thickness. Appeals to the first strategy are the motivation for recent reports of onedimensional (1D) nanorod and nanotube photoanodes with relatively large crystalline domains compared with nanoparticle photoanodes.¹¹⁻¹³ Although yet to be tested with alternative redox couples, these novel photoanodes show potential to collect electrons over greater distances relative to their nanoparticle analogues. While the second strategy is straightforward, reducing the photoanode thickness results in a proportional reduction in surface area for dye loading, ultimately resulting in loss of light harvesting efficiency (unless suitable superchromophores are devised). Overviews of the intricacies of DSSC charge dynamics, as modeled by the continuity equation for DSSCs, have been communicated by several authors.14-17

Herein, we introduce a new device architecture in which the electron collection distance is reduced by orders of magnitude, without a significant reduction in surface area,

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^{*} To whom correspondence should be addressed. E-mail: j-hupp@northwestern.edu.

[†] Northwestern University.

^{*} Argonne National Laboratory.



Figure 1. Idealized two-dimensional cross section of (a) a nanotube DSSC photoanode and (b) a concentric nanotube DSSC photoanode with ITO lining the pores.

by extending the transparent electrode deep within the semiconductor framework. The architecture is an elaboration of a previous design¹² in which wide bandgap semiconductor tubes were grown within the pores of anodic aluminum oxide (AAO) by atomic layer deposition (ALD), Figure 1a. By leveraging the broad synthetic palette of ALD, a variety of metal oxides tubes may be fabricated inside AAO including transparent conducting oxides (TCOs).¹⁸⁻²⁰ In this report, indium tin oxide (ITO) is grown within and upon AAO to produce a transmissive and conductive high-area electrode. Subsequent deposition of amorphous TiO₂ within the pores of the electrode yields a high-area photoanode suitable for implementation in DSSCs. A highly idealized schematic of the new architecture is shown as Figure 1b. In contrast to a conventional DSSC photoanode, in which electrons diffuse the length of the micrometers-thick nanotube or nanoparticle photoanode, we hypothesized that electrons would flow radially through a few nanometers of semiconductor to be collected by the adjacent, interdigitated ITO electrode (i-ITO). While the idea of extending the current collector within a DSSC photoanode has been previously postulated,²¹ this work represents the first experimental realization. Furthermore, the materials and methods as well as nanoscale architecture reported herein are considerably different from those previously proposed.

In this report, we demonstrate the viability of the new photoelectrode geometry with a conventional redox couple (I^-/I_3^-) by comparing its photovoltaic performance to that of control devices lacking ITO within the pores, Figure 1a. In many ways, the new nanoscale architecture is a return to simple bilayer heterojuction photovoltaics. The advantage here is that a large surface area (and correspondingly large dye-based light harvesting platform) can be packed into a small projected area by "folding" the heterojuction over a transparent template.

Materials and Methods. A nominally 60 μ m thick AAO membrane with 200 nm pores that is 25–50% porous (Anodisc 13, 0.02 μ m, Whatman) was cleaned by 30 s sonication in water followed by an isopropanol rinse. The membranes were coated using atomic layer deposition (ALD) in a custom built viscous flow reactor²² operating under 1 torr of N₂. For i-ITO samples, the membranes were coated with ~7 nm of ITO by ALD via 45 alternate exposures to

cyclopentadienyl indium (InCp) and O₃ at a temperature of 220 °C. Reactant exposure times of 15 and 80 s were employed, respectively, with nitrogen purge periods of 10 and 5 s after respective exposures. Doping of the In₂O₃ was accomplished by substituting pulses of tetrakisdimethylamido tin (TDMASn) and H₂O₂ every 10th cycle. The exposure times for TDMASn and H₂O₂ were 15 s and 10s, respectively, with 10 s N_2 purge periods following each exposure. Postdeposition annealing of the membrane under N2 in a tube furnace at 450 °C for 20 min increased the conductivity of the i-ITO. A thick (\sim 230 nm) ITO coating was applied to one side of both the i-ITO and the uncoated membranes by ion-assisted deposition (IAD) at room temperature.²³ IAD is a line-of-sight deposition method similar to sputtering in which very little ITO penetrates the tapered (20 nm) pores of the asymmetric membrane. Finally, several different thicknesses of amorphous TiO2 were deposited within each membrane using ALD. During the TiO₂ ALD, the membranes were mounted in a steel fixture designed to minimize TiO₂ growth on the thick IAD ITO layer (to which electrical contact is subsequently made). The TiO₂ ALD was performed using alternating exposures to TiCl₄ and H₂O at 100 °C for 6 and 3 s respectively, with nitrogen purges of 10 and 14 s between exposures. Finally, each sample was soaked in an ethanoic solution of 0.5 mM (Bu₄N)₂[Ru(4-(COOH),4'-(COO)-2,2'-bipyridine)₂(NCS)₂] ("N719", Dyesol, B2 dye) for 20 h and rinsed with ethanol before devices were assembled according to established methods.¹ Briefly, a 25 µm Surlyn spacer (Solaronix SX1170-25) was sandwiched between the photoanode and a platinized fluorine doped tin oxide (FTO) dark electrode. A 0.28 cm² active area was defined by a hole punched through the spacer, which softens at \sim 80 °C to seal the device. Electrical contact was made to each electrode by applying silver epoxy (SPI) to a tin-coated copper wire. A solution of 0.5 M butylmethylimidizolium iodide (TCI America), 0.05 M I₂, and 0.5 M tert-butylpyridine in 85:15 acetonitrile/valeronitrile was introduced into the cell via vacuum backfilling through a hole in the platinized FTO electrode. A second Surlyn spacer and microscope coverslip were sealed over the hole with a soldering iron. All chemicals were used as received from Sigma-Aldrich unless otherwise specified.

Monochromatic illumination for photoresponse experiments was achieved through the excitation slits of a Jobin-Yvon fluorescence spectrometer. J-V plots were acquired on a CH Instruments 1202 potentiostat. The AM 1.5 power efficiency of a 0.28 cm² masked device was measured on a class A solar cell analyzer from Spectra-Nova Technologies. Scanning electron microscopy (SEM) was performed using a Hitachi S4800.

Results and Discussion. The viability of the i-ITO photoanode design relies upon the ability of ALD to coat the membrane pores conformally with transparent and conductive ITO. The development of ALD processes for In₂O₃ and SnO₂ using nonhalogenated precursors make possible this challenging task.^{18–20} That the photoelectrodes employed herein are coated by a continuous film is demonstrated by resistivity measurements through the 60 μ m membranes. Though each nanoscale tube that spans a membrane is poorly conductive on its own, in parallel, the millions of tubes show a collective resistance <20 Ω , in analogy to a conductive copper cable composed of many thin filaments. Subsequent annealing in N₂ at 450 °C for 15 min reduces this value, measured by sandwiching the membrane between metal surfaces, to <3 Ω .

To prevent the rapid (parasitic) reaction of electrons in the ITO with the hole transporting ions in solution (I_3^-) , an equally conformal semiconductor layer is required. To apply this semiconducting layer, ALD of TiO2 was performed using alternating exposures to TiCl₄ and H₂O at 100 °C. This growth temperature was selected to minimize etching by TiCl₄.²⁴ Micro-Raman analysis performed after the TiO₂ ALD detected no anatase or rutile peaks on either the i-ITO or the bare alumina membranes (data not shown), implying that the semiconductor is generated in amorphous form as has been previously reported at this deposition temperature.²⁵ Cross sectional SEM analysis of a fractured alumina membrane with i-ITO (\sim 15 nm) and TiO₂ pore wall (\sim 20 nm) reveals the bilayer structure of the photoanode, Figure 2. The smoothness of the TiO_2 coating is further evidence for an amorphous film.

Figure 3 shows the short-circuit photocurrent densities (J_{sc}) measured for a series of samples prepared with increasing TiO₂ tube wall thickness, with and without i-ITO. The most salient observation is that all of the control samples, lacking i-ITO within the pores, exhibit very small current densities that only slowly increase with TiO₂ thickness. The meager output of the control devices is not surprising since injected electrons, forced to traverse the micrometers-long amorphous TiO₂ tubes, are expected to be collected extremely inefficiently at the distant TCO (see Figure 1a). We hypothesize that the diffusion/hopping of electrons through the amorphous semiconductor is too slow to compete effectively with their interception by I₃⁻. The result is a short effective diffusion length over which injected electrons may be collected and consequent poor conversion efficiencies. These small J_{sc} values are in contrast to the J_{sc} of a similarly constructed ZnO nanotube DSSC¹² in which the highly crystalline semiconductor provides an amply efficient electron transport pathway. An additional control, in which only i-ITO is





Figure 2. Cross-sectional SEM image of a TiO₂ tube cleaved to reveal the i-ITO tube beneath. The concentric tubes, grown by atomic layer deposition, are embedded in a $\sim 60 \ \mu m$ long alumina pore. In order to provide clear imaging, the TiO₂ and i-ITO film thicknesses shown here are thicker than those utilized in photovoltaic devices.



Figure 3. Short-circuit photocurrent density for devices with (blue, closed circle) and without (red, open circle) 7 nm of ITO in the pores as a function of TiO_2 tube wall thickness.

present in the pores, underscores the need for some thickness of semiconductor to rectify the dark current. Here, the high surface area TCO exhibits exceedingly large dark currents that overwhelm any photocurrent. In striking contrast, the i-ITO DSSCs (idealized in Figure 1b), each with >2 nm of TiO₂ over 7 nm of ITO within the pores, show larger J_{SC} values (10- to 60-fold) than the controls. Indeed, the current densities approach that of the best ZnO nanotube DSSCs to date.¹²

In comparison with the best nanocrystalline DSSCs, however, nanotube J_{SC} values are still low. Current densities of the nanotube devices are primarily limited by low light harvesting efficiency, a consequence of both the modest surface area of the AAO template (~400 cm²/cm²) and the propensity of the commercial AAO samples to scatter incident photons and thereby inhibit light penetration. Both problems are, in principle at least, solvable by switching to custom-made, narrow-channel AAO templates. The extent



Figure 4. Short-circuit photocurrent density (J_{SC}) of the most efficient i-ITO device reported (light green, closed circle) as a function of illumination intensity. The departure from linearity at high light intensity can be reduced by increasing the I_3^- to 0.15 M (dark green, open circle) in an otherwise identical device. The dashed line defines a hypothetical linear J_{SC} /incident-power correlation. Note that 100 mW/cm² corresponds approximately to 1 Sun of visible-region light intensity.



Figure 5. Open-circuit photovoltage for devices with (blue, closed circle) and without (red, open circle) ITO in the pores as a function of TiO_2 tube wall thickness.

to which photocurrents are ultimately limited by ion transport through the liquid electrolyte is revealed by light intensity dependence measurements, Figure 4. The photocurrent increases linearly with light intensity, up to approximately 4 mA/cm². Beyond this point, mass transport of I_3^- through the relatively thick photoanode (among other possible issues) evidently becomes detrimental. This effect can be ameliorated to some extent by increasing the concentration of I_3^- in solution (see Figure 4).

The photovoltage at open circuit (V_{OC}) of i-ITO devices is consistently greater than 700 mV, while the J-V curve of the control device (without TiO₂, but with i-ITO) passes through the origin. Apparently ≤ 2 nm of TiO₂ is required to reach the maximum photovoltage for the system, Figure 5. Amorphous TiO₂ tube DSSCs (without ITO in the pores) also exhibit good photovoltages that peak (or perhaps plateau) around 9 nm. The V_{OC} of the most efficient i-ITO DSSC reported here is within 80% of the V_{OC} of that for the most efficient nanoparticle DSSCs to date (850 mV)¹ implying



Figure 6. J-V curve under AM 1.5 illumination and in the dark for the most efficient i-ITO device, 5 nm TiO₂ over 7 nm ITO.



Figure 7. Representative normalized photoresponses under shortcircuit conditions for devices with (blue, closed circle) and without (red, open circle) ITO in the pores.

that the rectifying behavior of amorphous TiO_2 is similar to that of anatase TiO_2 (employed in nanoparticle cells).

The J-V curve of the most efficient device, 5 nm TiO₂ over 7 nm ITO, is shown as Figure 6. Under AM 1.5 illumination, the DSSC exhibits a J_{SC} of 2.6 mA/cm², V_{OC} of 716 mV, and fill factor (*FF*) of 0.58, yielding an overall conversion efficiency of 1.1%. The modest *FF* is most likely due to a low shunt resistance, the signature of which can be seen in the negatively sloping photocurrent at low applied potentials. The origin of the shunt resistance is not yet clear.

Further insight into the operation of the new photoanodes can be gleaned from the spectral photoresponse of the completed devices. Because of the logarithmic relationship between absorbance and light harvesting efficiency, a longer path length cell will exhibit broadened features on a light harvesting efficiency scale. For example, the difference between a 1 and 2 absorbance unit valley and peak is small (~10%) on the light harvesting (1 minus transmission) scale. This effect can be seen in Figure 7 around the peak absorbance (~0.7) at 530 nm. It appears that the path length (or in this case "useful" photoanode thickness as reported by photoresponse) is significantly shorter for devices without ITO in the pores. A deviation from this simple analysis is evident between wavelengths 400-430 nm where the i-ITO device has a smaller (normalized) photoresponse. This effect is most likely due to the attenuation of blue light traversing the cell by I_3^- and by ITO within the pores. The observation further supports the hypothesis that the useful photoanode thickness is greater for the DSSCs with ITO in the pores.

The substantially larger J_{SC} and broadened photoresponse spectra of i-ITO DSSCs indicate that electrons are being collected over a much longer distance than in the control DSSCs. That is, radial electron collection through amorphous TiO₂ (to surrounding ITO) competes effectively with electron interception, even deep within the photoelectrode. In contrast, longitudinal electron diffusion through amorphous TiO₂ (to a distant planar ITO layer) does not. While the control devices based on amorphous TiO2 tubes were anticipated to perform poorly, the recovery of photovoltaic performance upon interdigitation of a TCO is remarkable and points to a broader utility for the radial collection approach. Particularly intriguing is the prospect of replacing traditional crystalline semiconductors (like anatase TiO₂) with previously unexplored DSSC photoanode materials; amorphous TiO₂ is now the first on this list. Since the task of rapid charge transport has been transferred to the i-ITO, effective use of otherwise inefficient materials such as electrodeposited and/or amorphous indirect bandgap semiconductors (including, perhaps, fuel producing photoelectrodes) may be feasible. Furthermore, the processing temperatures of DSSC photoanodes may be relaxed to allow for greater substrate compatibility.

In summary, a promising new DSSC photoanode design in which electrons radially diffuse through the wall of semiconducting tubes and are efficiently collected at an adjacent, concentric ITO tube has now been experimentally realized. Atomic layer deposition is used to grow the current collector within an AAO template and subsequently coat the high surface-area electrode with a semiconducting metal oxide. The viability of this architecture as a DSSC photoanode is demonstrated by photovoltaic characterization of completed devices with and without the interdigitated current collector. This architecture is not limited to AAO templates, and the benefits of radial charge collection may also be realized using other high surface area templates such as aerogels.²⁶ Like previously reported nanotube array based DSSCs, the power efficiencies of these DSSCs are limited by modest light harvesting efficiency (at least with conventional dyes). Current work is focused on determining the extent to which optimized, higher-area templates can be translated into better photoelectrode performance and the extent to which the idea can be used to advantage with chemical producing (e.g., water oxidizing) photoelectrodes. Finally, these systems are good candidates for the exploration of alternative redox shuttles and previously unexplored semiconducting materials due to their potential for fast electron collection via the adjacent TCO.

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